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Fujimoto, Y.; Re, P.L.; Miyamoto, M.;
Solid-State Circuits, IEEE Journal of
Volume 40, Issue 9, Sept. 2005 Page(s):1865 - 1871
Digital Object Identifier 10.1109/JSSC.2005.848145[AbstractPlus](#) | Full Text: [PDF](#)(760 KB) IEEE JNL

2. A delta-sigma modulator for 1-bit digital switching amplifier [audio power]

Re, P.L.; Fujimoto, Y.; Tani, H.; Miyamoto, M.;
Custom Integrated Circuits Conference, 2004. Proceedings of the IEEE 2004
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